



06/28/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

Best Available Copy

U.S. UTILITY Patent Application

O.I.P.E. 1CW SCANNED <u>141</u> Q.A. <u>UR</u>	PATENT DATE
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APPLICATION NO. 09/892566	CONT/PRIOR	CLASS 711	SUBCLASS 118	ART UNIT 2185 2180	EXAMINER J. K. PORTKA
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Power reduction for processor front-end by caching decoded instructions

ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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